

Integrated Circuit and Information Technology Pvt. Ltd., ISO 9001 : 2008 CERTIFIED

FEMDP in Embedded Systems

Topic	No. of hours
Semester I	
Project-based Learning IT	
DBMS: relational and object models, database schema, queries	180 hrs
Software Engineering	
WEB architecture: client server, communication protocols	
HMI: ergonomy, dynamic contents generation, formatting	
Network Fundamentals	45 hrs
Network communication	
Layer approach	
OSI model	
TCP/IP model	
Network devices	
 Network addressing models 	
Communication channel	
Signal Processing	25 hrs
Frequency analysis: FFT	
Digital Filtering: filters, linear prediction	
Interpolation, Decimation	
Java Programming	50 hrs
Management Training	50 hrs
Economic Principles	
Intercultural relations	
Corporate organization	
International sales	
Communication, negotiation	
French Language Course	60 hrs
Total	410 hrs

Semester II	
Architecture and VLSI Design	
Internal architecture of a RISC microprocessor	50 hrs
Digital VLSI circuit design	
• Labs	
Advanced Operating Systems	25 hrs

Multitask system, process and task scheduler	
Memory management	
• File system	
Computer Architecture	25 hrs
Buses - the address decoding	
Interruptions - the start of a system	
Exchanges on DMA mode	
Cache memory	
Virtual memory	
Introduction to DSP	
• Labs	
Microcontroller and power consumption optimization	
System Programming	25 hrs
Multitask system, process and task scheduler	
Memory management	
File system	
Project and Introduction to Research	60 hrs
The project is composed of a case study. The students will be called	
upon to use the knowledge, design techniques and tools that they	
learnt through their course. Students interested in research can join	
one of ISEP's research team to work directly with faculty members	
Management Training	50 hrs
Supply and Demand	
Firms and Markets	
The Government and the Economy	
Macroeconomics: Introduction	
Monetary and Fiscal Policy	
The Open Economy	1
French + English Language course	(60+25) hrs
Total	320 hrs

Semester III	
Automatic Control	
System model	25 hrs
State space	
 Optimum command theory 	
 States representation 	
 Consideration of random phenomena 	
Embedded Calculators	50 hrs
 Embedded bus (1553 bus, CAN bus) 	
 Architecture od distributed computing 	

- David Destateming	
Rapid Prototyping	
Internal and external bypass, fullpath systems	
Embedded calculators and their design	
Constraints and Systems Implementation	50 hrs
Methodology development cycles and systems	
Life cycle of software / hardware	
System Simulation	
Tools for formal proof	
Real-time UML	
Safety and Risk Analysis	25 hrs
Failure trees - failure density, failure rate	
Reliability of components, boards, systems, life duration, physical	
failure analysis - methods and tests	
Redundant systems, serial, parallel, vote, triplication	
Coded systems	
Standards on quality, standards on safety	
Electromagnetic compatibility of systems	
Reliability	25 hrs
Reliability of components	
Reliability of cards	
Life cycle	
System Implementation	25 hrs
Algorithm implementation (complexity, specific architectures)	
Standards and low consumption communications modules	
Project	50 hrs
French Language Course	
French Language course	60 hrs
Total	310 hrs

Semester IV	
Internship	
 The internship with an international company will enable to display valuable professional skills and attitudes developed during the three academic semesters. ISEP will help you in finding an internship. Companies usually give a stipend to the trainees. 	6 months